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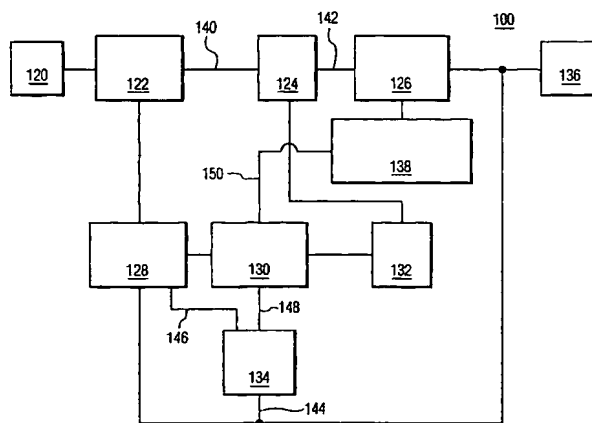
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- (54) Title:** ELECTRONIC BALLAST WITH OPEN CIRCUIT VOLTAGE REGULATION



- (57) Abstract:** An electronic ballast includes open circuit voltage regulation comprises an filament current sensing circuit (224) operably connected to an output of the electronic ballast and generating a sensed output voltage signal, and a regulating pulse width modulator U3 receiving the sensed output voltage signal and operably connected to control voltage at the output of the electronic ballast, the regulating pulse width modulator U3 having an output voltage threshold limit. The regulating pulse width modulator U3 limits the voltage at the output of the electronic ballast when the sensed output voltage signal exceeds the output voltage threshold limit. The regulating pulse width modulator U3 can limit the output voltage by limiting the pulse width to the high voltage driver and the resonant half bridge. The filament current sensing circuit (224) can sense the output voltage indirectly, such as by sensing tank current, or can sense the output voltage directly.

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SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

- *as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ,*

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- *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations*

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